

FIG. 1

FIG. 2

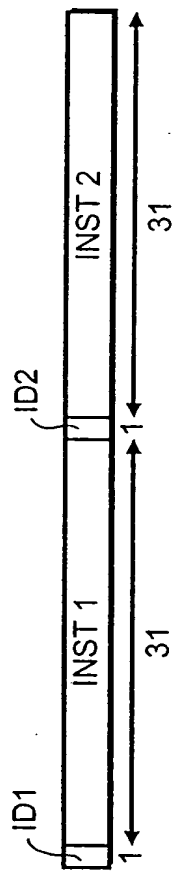


FIG. 3

ID BIT

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	09	08	07	06	05	04	03	02	01	00
M	G		OPCODE						S	TST				SRC2							SRC1								DST		(1)
M	G		OPCODE						TST					IMMED							SRC1								DST		(2)
M	G		OPCODE						S	TST				SRC2							SRC1				SIGN		M REG				(3)
M	G		OPCODE						S	TST				SHIFT		M REG					SRC1							DST			(4)
M	G		OPCODE						S	TST				SRC2							SRC1							DST			(5)
M			OPCODE				T							IMMEDIATE OFFSET											BASE REG				DST		(6)
M			OPCODE											INDEX OP	T		INDEX REG								BASE REG				DST		(7)

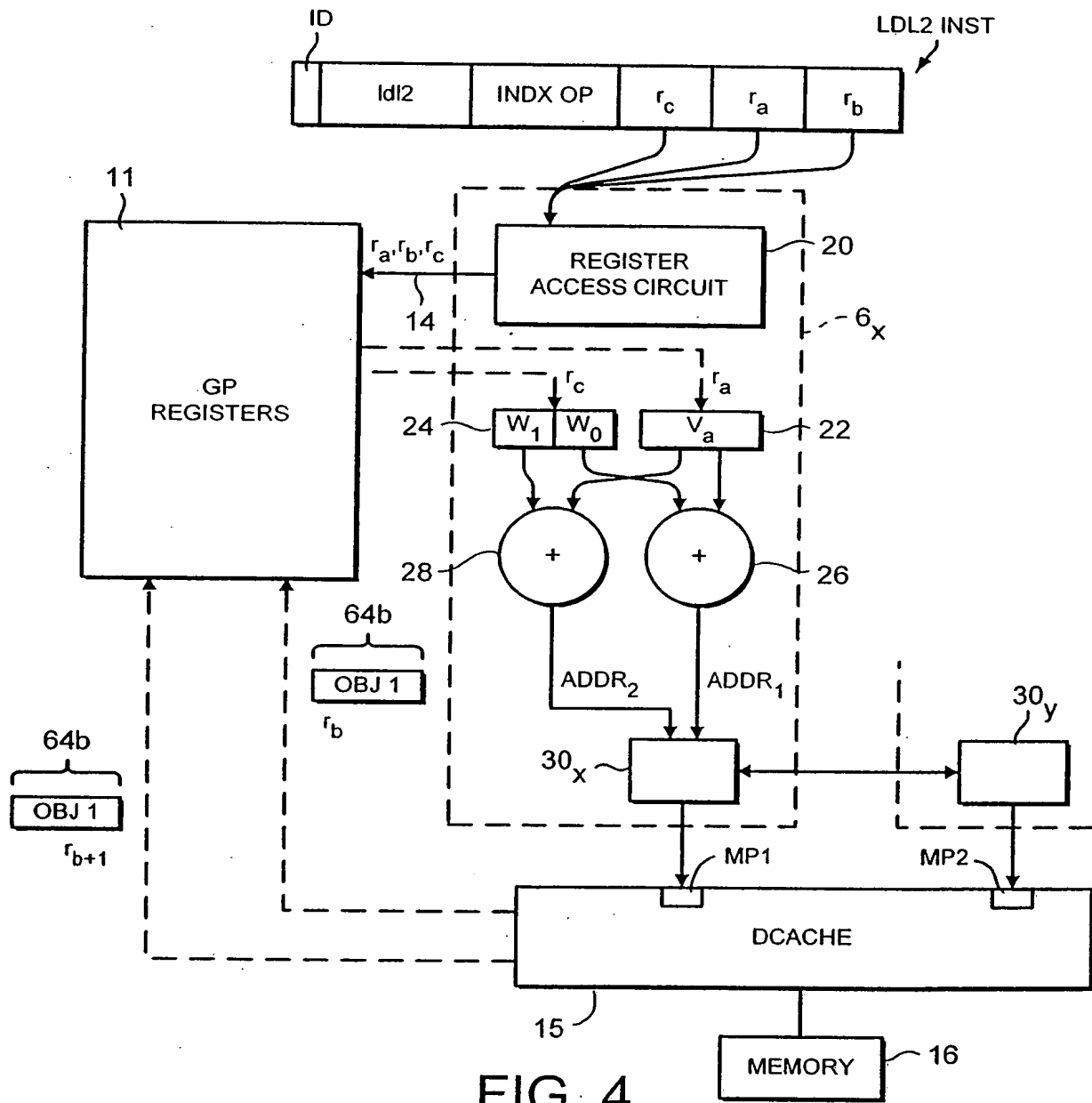


FIG. 4

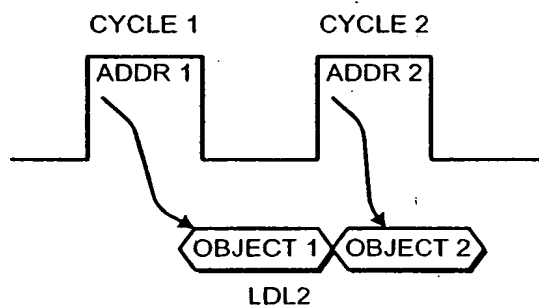


FIG. 5a

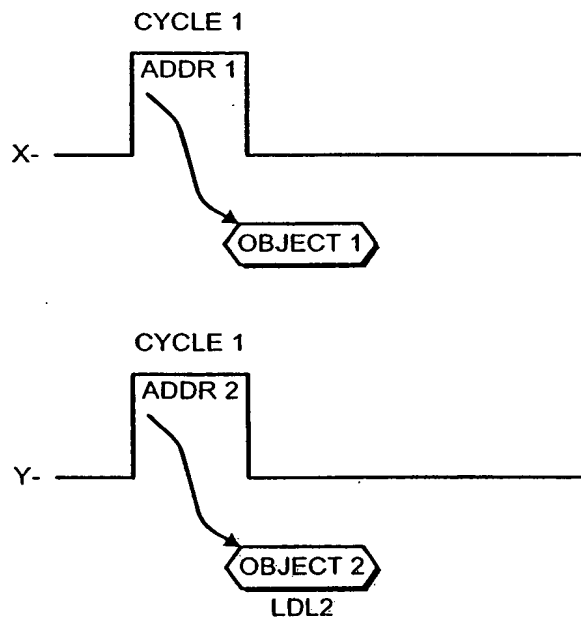


FIG. 5b

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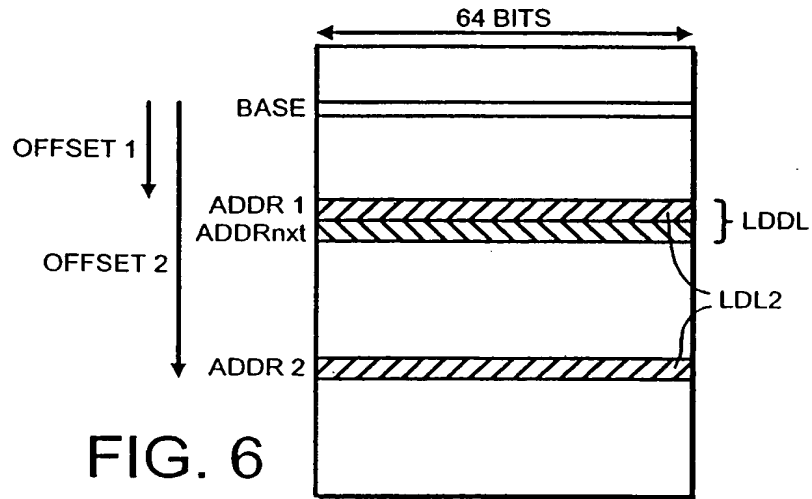


FIG. 6

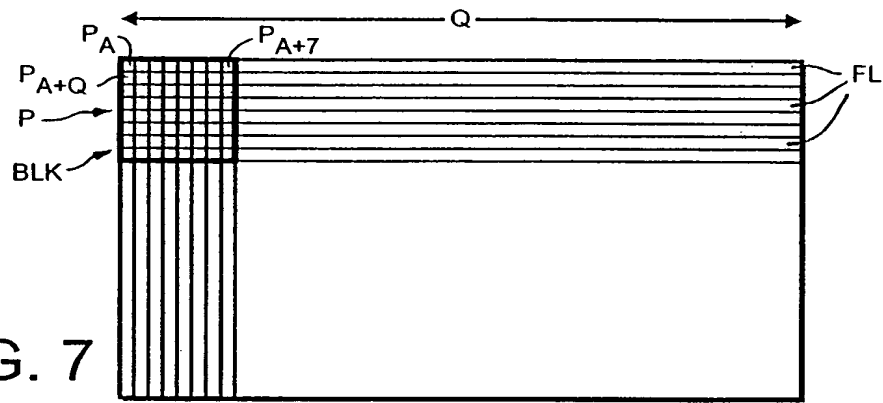


FIG. 7

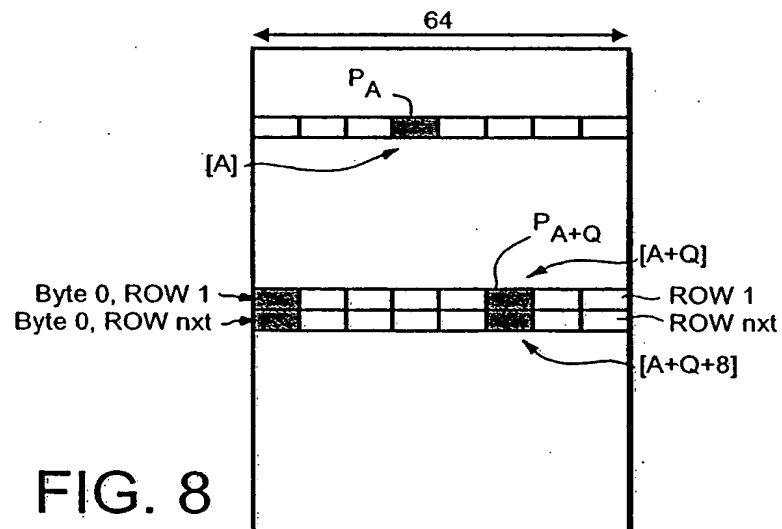


FIG. 8